Operating Systems Design for Embedded Environments

**Practica 1 0.0 OS Tick**

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| --- | --- |
| **Project:** | **OS Tick** |
| **Team:** | **X** |
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| **Comments:** |  |

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# SW Conceptual design

# Function Description and Dynamic Behavior

## Function void Mcu\_Init (void)

|  |  |
| --- | --- |
| **Description** | *Void Mcu\_Init(void);* |
| **Return Value** | *None* |
| **Precondition** | *None* |
| **Post condition** | *None* |
| **Error Conditions** |  |

**Dynamic Behavior**

Mcu\_Init API initializes the PLL or XTAL frequency configuration.

## Function void Mcu\_InitClock (void)

|  |  |
| --- | --- |
| **Description** | *Void Mcu\_InitClock(void);* |
| **Return Value** | *None* |
| **Precondition** | *None* |
| **Post condition** | *None* |
| **Error Conditions** |  |

**Dynamic Behavior**

Mcu\_InitClock API enables XTAL Clock as the main clock source.

## Function void Mcu\_DistributePllClock (void)

|  |  |
| --- | --- |
| **Description** | *Void Mcu\_DistributePllClock(void);* |
| **Return Value** | *None* |
| **Precondition** | *None* |
| **Post condition** | *None* |
| **Error Conditions** |  |

**Dynamic Behavior**

Mcu\_DistributePllClock API enables PLL as the main clock source.

## Function void Gpt\_Init (void)

|  |  |
| --- | --- |
| **Description** | *Void Gpt\_Init(const Gpt\_ConfigType\* ConfigPtr);* |
| **Return Value** | *None* |
| **Parameters** | *ConfigPtr : pointer to the initial configuration structure* |
| **Post condition** | *None* |
| **Error Conditions** |  |

**Dynamic Behavior**

Gpt\_Init API initializes PIT according *ConfigPtr* configuration.

## Function void Gpt\_StartTimer( Gpt\_ChannelType Channel, Gpt\_ValueType Value )

|  |  |
| --- | --- |
| **Description** | *void Gpt\_StartTimer( Gpt\_ChannelType Channel, Gpt\_ValueType Value );* |
| **Return Value** | *None* |
| **Parameters** | Channel*: pointer to the initial configuration structure* |
| **Post condition** | *None* |
| **Error Conditions** |  |

**Dynamic Behavior**

Gpt\_Init API initializes PIT according *ConfigPtr* configuration.